Until UCC is Here - UCG Status Update





Outline

1. UCG status (and how did we get to it)

2. UCG in the software stack

3. UCG & UCC – where is this going?

4. Huawei's roadmap for collective operations



A Brief history of UCG (*G for Groups*) and UCC

- [Aug. 2018] UCG Started (just me, soon after joining Huawei)
- [Dec. 2018] UCG API submitted for upstreaming (#3091#3602)
- [Nov. 2019] Talks about upstreaming UCG (<u>#4545</u>) didn't work out...
- [Dec. 2019] UCF formed the UCC working-group
- [Sep. 2020] Huawei Cloud officially publishes its

"High-Performance Communication Library"

- [Sep. 2020] UCC's external API finalized (<u>#1</u>)
- [Dec. 2020] ... this update.

	CAE 气象 超算 其他	
行业应用		
	Open MPI	
HMPI	MPI点对点通信 MPI集合通信 ob1 ucx	
нисх	Open UCX UCP UCG UCT UCS	
OFED	Verbs	
基础硬件	网络: IB, RoCE	
Cate Hour Port 1	架构: ARM, X86	



*Any UCX version?! Yes. See next slide.

Current Status

Right now - there are too many versions!

- 1. my personal UCG repo (deprecated, finally ☺)
- 2. xUCG (<u>OpenUCX github</u>)
 - This is my "master" branch should fit **ANY*** UCX version, not 100% stable...
- 3. Huawei's xUCG (<u>Huawei's github</u>)
 - Recently created by the team in China assumes UCX v1.6, very stable
- 4. Huawei's internal UCG (Steady release schedule within <u>Hyper-MPI</u>) - Includes some proprietary extensions, but mostly just experimental code



Supports:

- a range of collectives (Bcast, Reduce, Allreduce, Barrier, Scatter, Gather, Allgather),
- any datatype Open-MPI does,
- any transports UCT does,
- any protocol UCP does, (not even close... but we're working on it!)
- various hardware platforms.

Also: tested up to 256 (x86) nodes and up to 256 (ARM) cores on a host.



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UCG in the software stack

APP	OpenFOAM WRF		NAMD			
	MPI P2P MPI	MPI layer Collective ned	ORTE	-		
Open MPI						
Open UCX	UCP Tag- matched send/recv Active Messages UCT Transport: sm, Kunpeng, dc,	UCG Topology- ware algorithms Plan&Phase , rc, ud, rc/ud for knem	SHM- based algorithms trigger/exec			
OFED	Verbs]				
HW	Driver librdmacm libibverbs Lib for kunpeng NIC (IB/RoCE)					



Making UCG work with ANY UCX version

Challenge*: keep using UCX internal APIs (UCS / UCT) as it evolves

Incident #1 - Change-ID 8da6a5be2e:

- typedef void (*uct_completion_callback_t)(uct_completion_t *self,

ucs_status_t status);

+ typedef void (*uct_completion_callback_t)(uct_completion_t *self);

Incident #2 - Change-ID fca960826a:

- #define UCS_CONFIG_REGISTER_TABLE_ENTRY(_entry) \
- + #define UCS_CONFIG_REGISTER_TABLE_ENTRY(_entry, _list) \

• • •

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Making UCG work with ANY UCX version

Challenge*: keep using UCX internal APIs (UCS / UCT) as it evolves *Not as hard as it sounds, actually.

Incident #1 - Change-ID 8da6a5be2e:

Incident #2 - Change-ID fca960826a:

AC_COMPILE_IFELSE([AC_LANG_PROGRAM([[#include "ucs/config/parser.h"]],

[[#undef UCS_CONFIG_REGISTER_TABLE_ENTRY]
[#define UCS_CONFIG_REGISTER_TABLE_ENTRY(a, b)]
[UCS CONFIG REGISTER TABLE(NULL, NULL, NULL, ...



APIs

- "Northbound":
 - Generic (src/ucg/api/ucg.h): ucg_group_create, ucg_collective_create
 - MPI-specific (src/ucg/api/ucg_mpi.h): MPI_Reduce(),
 - Versioning (src/ucg/api/ucg_version.h): similar to ucp_version.h
- "Southbound":
 - Plan Components (src/ucg/api/ucg_plan_components.h):
 - for accommodating multiple implementations (similar to OMPI's MCA COLL)
 - Expected components: "builtin" (day-1), hicoll, NCCL?



Northbound API, side-by-side (Blocking)

MPI Application (or MPI-specific API)	UCX Groups (UCG) Equivalent (Generic API)
MPI_Bcast (bufA,, rowcomm)	<pre>paramsA.buf = bufA; paramsA.type.modifiers = BCAST_MODIFIERS; </pre>
	<pre>ucg_collective_create(rowGroup, &paramsA, &collA); ucg_collective_start_nb(collA, &reqA);</pre>
	<pre>MPI_Wait(reqA, &status);</pre>

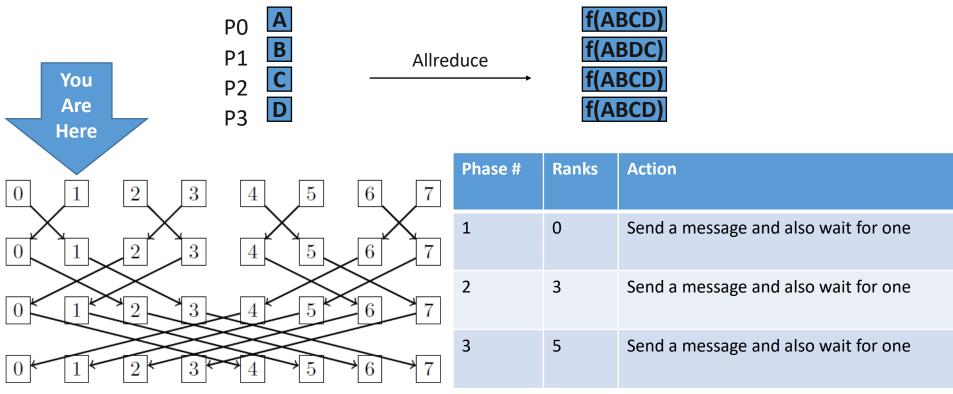


Northbound API, side-by-side (Non-blocking)

```
MPI Application (or MPI-specific API)
                               UCX Groups (UCG) Equivalent (Generic API)
MPI Ibcast init(..., &req);
                               ucg collective create (Group, ..., &coll);
for (i=0; i<MAXITER; i++) { for (i=0; i<MAXITER; i++) {
    compute(buf);
                                    compute(buf);
                                    ucg collective start nb(coll, &req);
    MPI Start(req);
    MPI Wait(req, &status);
                                    MPI Wait(req, &status);
                                ucg request free(req);
}
MPI Request free(req);
```



Decoupling Planning and Execution – Recursive Doubling





Decoupling Planning and Execution – Tree-based

Phase #	Ranks	Action		Phase #	Ranks	Parameters
1	2,3	Wait for a message	\rightarrow	1	2,3	Action: Wait for a message from each node
2	0	Send a message				Incoming buffer: temporary buffer of
3	0	Wait for a message				bytes which starts with the user's inpu Incoming length: 16 bytes Incoming offset: 8 bytes
4	2,3	Send a message				C ,
You Are Here	0			2	0	Action: Send a message to each node Outgoing buffer: the temporary buffe from the previous step Outgoing length: 24 bytes Outgoing offset: 0 bytes
		4	8-byte call to MPI_Gather()			
3		5 6	*Is tree the be	st choice	e for M	PI_Gather? Not always

Decoupling Planning and Execution – Tree-based

Phase #	Ranks	Action	Phase #	Ranks	Parameters
	2,3	Wait for a message	3	0	Action: Wait for a message from each node
	0	Send a message			Incoming buffer: destination buffer
	0	Wait for a message			Incoming length: 8 bytes Incoming offset: 0 bytes
	2,3	Send a message			
e			4	2,3	Action: Send a message to each node Outgoing buffer: destination buffer Outgoing length: 8 bytes Outgoing offset: 0 bytes
1		4	8-by	yte call	to MPI_Bcast()
3)	5 6			TEL A'

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UCC is coming about, slowly – it'll be a while before we can use it, and...

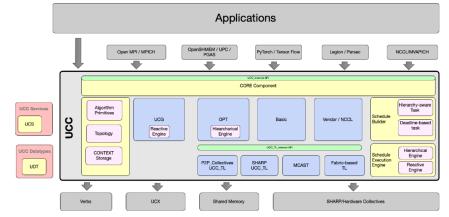




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UCC looks *to me* like a big performance risk!

- UCC adds levels of abstraction, possibly returning issues from Open-MPI
 - Example #1: UCC is limited to UCP API... how/when to use atomics for reduction?
 - Example #2: progress is not aware whether SHARP is used at the moment.





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- UCC adds levels of abstraction, possibly returning issues from Open-MPI
 - Example #1: UCC is limited to UCP API... how/when to use atomics for reduction?
 - Example #2: progress is not aware whether SHARP is used at the moment.
- Development is "breadth-fist" instead of "depth-first"
 - Preliminary performance results are not expected any time soon.
- UCC is practically just two participants: Mellanox and Huawei.
 - Missing representation from other vendors and potential users.



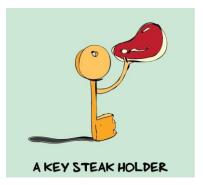
Challenges, looking forward

- 1. Must show the approach is beneficial before investing much effort
 - I don't think xCCL's or xUCG's individual performance is proof enough...



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 - More contributing parties
 - More representation for use-cases
 - More exposure, feedback, stakeholders...





Challenges, looking forward

- 1. Must show the approach is beneficial before investing much effort
 - I don't think xCCL's or xUCG's individual performance is proof enough...
- 2. Community involvement:
 - More contributing parties
 - More representation for use-cases
 - More exposure, feedback, stakeholders...



3. Delivering relevant, high-quality software.

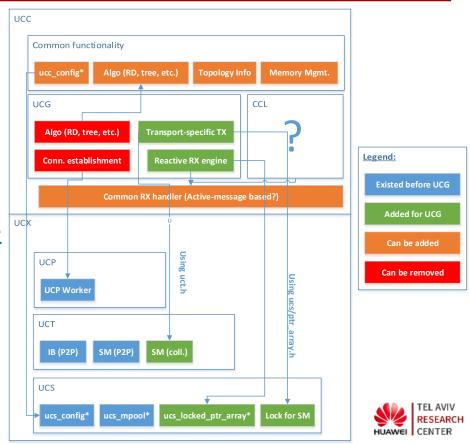


UCG

Not going anywhere, anytime soon... no open-source alternative (yet)

3 possible paths:

- 1. Merge into UCC
- 2. Merge into <u>Huawei's UCX package</u>
- 3. Merge into upstream UCX



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Development Roadmap

- Hardware-specific capabilities
 - Kunpeng CPU
 - Storage product-lines
 - Atlas AI accelerator product-lines
- Integration with related HPC Software
 - HPC Storage and parallel filesystems, e.g. MPI I/O
 - Batch scheduler, e.g. SLURM (for job information)
 - Builders, e.g. *Spack* (provide collectives for other packages)
- Custom acceleration hints (through MPI?)
 - Persistent operations
 - Read-only / write-only buffers





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